

السيرة الذاتية CURRICULUM VITAE

للدكتور: عزالدين محمد سويسى السلامى (استاذ دكتور- كلية تقنية المعلومات جامعة طرابلس- ليبيا)

Azeddien M. Sllame

Personal Information

- Name: Azeddien M. Sllame
- Full Professor
- Mobile: +218-92-8592383
- Email: aziz239@yahoo.com, a.sllame@uot.edu.ly
- PhD in Information Technology
- Google Scholar :
- <https://scholar.google.com.ly/citations?user=leoboecAAAAJ&hl=en&oi=ao>
- Research gate web page:
https://www.researchgate.net/profile/Azeddien_Sllame3/contributions



Work Experience

- Full Professor at Faculty of Information Technology since 5.2.2022
- Head of higher educational department at Faculty of Information Technology since 1-1-2022
- Associate Professor at Faculty of Information Technology 05.02.2018----5.2.2022
- Head of the Quality and Assessment office for Educational Programs at University of Tripoli (Feb. 2015- August 2017)
- Faculty of Information Technology, University of Tripoli, Libya
- Member of the curriculum committee of Faculty of Information Technology (2013-now)
- Head of web technology department at Faculty of Information Technology, Tripoli 2014-1016
- Head of computer network department (2013-2014)

Educational Qualification

- PhD, *Information Technology*, Brno University of Technology, Faculty of Information Technology, *Czech Republic* (Feb. 2003).
- MSc (very good), *Computer Science and Engineering*, Brno University of Technology, Faculty of Electrical Engineering and Computer Science, *Czech Republic* (Sep. 1997)
- BSc (very good), *Computer Engineering*, Faculty of Engineering, *Tajoura, Libya* (Sep. 1990)

Fields of Interest

- Cyber security and security policies according to ISO 27xxx series
- ISO/IEC 27005 Information Security Risk Management Training
- System-on-chip and network-on-chip designs
- Cloud computing and Fog computing security
- Computer networks: security, protocols, processes, technologies, and quality of service

- Data centers infrastructure design
- Bio-inspired computing (evolvable hardware)
- Advanced computer architectures
- Curriculum development
- Quality in higher education
- Accessibility in International Higher Education

Working Experience

- Cybersecurity implementation according to ISO 27000 series guidelines
- Information security management system Implementation guidance according to ISO 27xxx
- Keynote speaker about Quality in higher education at University of Tripoli
- Trainer in how to develop, organize, evaluate educational programs and courses according to quality policies
- Assessment and evaluation of higher education programs and courses
- Trainer in strategic planning and quality in higher education (SWOT analysis)
- Teaching different computer science and information technology courses (2003-current)
- Consultant on the area of quality management in higher education
- Member of Libyan national information technology (ISO) standardization committee (2008-2011)
- Data center infrastructure design and implementation for many Libyan organizations (2003-2016)
- Primary consultant for Libyan Army Procurement Department for advanced communication systems, border control systems, C3I & C4I systems, and data centers (2007-2013)
- Networking engineer at data center (2003-2007)
- MSc & PhD student (1996-2003)
- Computer maintenance engineer (1991-1995)

Awards

- Faculty award 1990 (BSc in 2nd place)

Capability of Training: I can teach and train different people, engineers in the following courses in (Arabic or English)

- Cyber security and ethical hacking
- Security standards in IT systems (ISO 27xxx series)
- Security policy “ understanding and preparation of different security policies”
- Network security principles
- Principles of strategic administration
- Principles of strategic planning
- Leadership and development
- Strategic planning of comm. Systems
- Network modeling and simulation tools (GNS3, Packet tracer, Wireshark, OPNET, EVE, UNL, Mininet)
- Multimedia networking and Performance evaluation of multimedia over IP/MPLS networks
- Network design
- Embedded systems design

- Network QoS
- MPLS networking technique
- Quality in Higher Education

Technical courses

- Security standards in IT systems (ISO 27xxx series)
- Cyber security
- Network security
- Ethical hacking
- Quality management ISO9000
- IT Project management
- Strategic administration
- Strategic planning
- Leadership and development
- Communication systems acceptance procedures (step-by-step from initial design to field)
- Strategic planning of comm. systems
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Teaching Courses

- Security policy according to ISO 27000 family
- Computer networks (LAN, WAN, Fiber networks, Wireless, wireless ad hoc networks, WiMax, ...)
- Modeling and performance evaluation of computer networks
- Routing and switching protocols in computer networks
- QoS of computer networks
- Multimedia over IP-based computer networks
- Security of information and computer networks
- Operating systems: concepts and design
- Computer organization and design
- Embedded systems: specification, design and prototyping with FPGA
- Quality management in higher education
- Advanced computer architectures
- Data centers for E-government and E-commerce
- System-on-Chip and Network-on-Chip systems: concepts and design
- High-level synthesis design and FPGA
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Professional Tools

- EVE Network Lab tool
- UNL Network Lab tool
- OPNET standard computer network modeling and simulation tool
- Packet tracer network tool
- GNS3 network design tool
- Wireshark network tool
- *OpenMPLS* modeling and simulation tool
- System-on-chip and network-on-chip design tools
- VHDL language to design digital systems with FPGA technology
- *Xilinx* software design tools for FPGA chips technology

- *Altera* software design tools for FPGA chips technology
- *Synopsys* software design tools for system-on-chip systems

MSc supervision

- A DDoS Attack Detection and Mitigation for Internet of Things within a Software-Defined Network Environment (2021-2022)
- Design of 2D-Mesh Network-on-Chip Systems Based on MPLS Networking Technique (2019-2020)
- Partitioning Technique for Network-on-Chip designs (2015-2016)
- Novel Network-on-Chip design architectures (2014-2015)
- Analysis of multimedia QoS in different computer networks (2013-2014)
- Evaluating Mesh interconnection networks for SOC architectures, Libya, 2012-2013
- Scheduling algorithms for core-based SOC systems, Libya, 2008-2010
- Evaluating Fat tree interconnection networks for SOC architectures, Libya, 2008-2010

Editorial board, organizing and reviewing of journal and conference papers

- Editorial board member of *International Journal of Wireless Communications, Networking and Mobile Computing*
- Conference program committee member of *the annual Middle Eastern Simulation and Modeling Conference (MESM) organized by: The European Multidisciplinary Society for Modelling Simulation Technology (www.eurosis.org)*
- Conference program committee member of *the IEEE International Symposium on Networks, Computers and Communications organized by IEEE-Tunis*
- Conference program committee member of the *International Conference on Mobile Wireless Technology (ICMWT)*

Publication list

الورقات البحثية للدكتور: عز الدين محمد سويسي السلامي

Dr. Azeddien M. SLLAME

Email: aziz239@yahoo.com, a.sllame@uot.edu.ly

[1]	Reema A. Saad, Mariam Abojella Msaad, Azeddien M. Sllame: Performance Evaluation of Multimedia Streaming Applications in MPLS Networks Using OPNET, in the IEEE 1st International Maghreb Meeting of the conference on Sciences and Techniques of Automatic control and computer engineering (IEEE MI-STA'2021), pp. 333-338, May 2021, Tripoli, Libya.
[2]	Mariam Abojella Msaad, Reema A. Saad, Azeddien M. Sllame: A Simulation based analysis study for DDoS attacks on Computer Networks , in the IEEE 1st International Maghreb Meeting of the conference on Sciences and Techniques of Automatic control and computer engineering (IEEE MI-STA'2021), pp. 756-761, May 2021, Tripoli, Libya.
[3]	Azeddien M. Sllame: QoS of communication networks using MPLS protocol, Book Chapter in a Book entitled: "Intelligent Wireless Communications", Edited by Mastorakis et. al, IET Academic Books publisher (www.theiet.org), pp.177-220, April 2021, UK.
[4]	Walid Mokhtar Salh, Azeddien M. Sllame: Applying Genetic Algorithm to Solve Partitioning and Mapping Problem for Mesh Network-on-Chip Systems, in International Journal of Computer Science & Information Technology (IJCSIT) Vol 13, No 1, February 2021 , pp.33-47.
[5]	Azeddien M. Sllame, Nagwa Salama, Hadeel Youns Ben Rajab: Applying MPLS Technique as On-Chip Communication Means for Network-on-Chip With Mesh Topology, in Proceeding of the 28th FRUCT Association (IEEE and ACM conference) , ISSN 2305-7254, pp.628-637, Moscow, Russia, 25-29 January 2021 (SJR 0.23),.
[6]	Nagwa Salama, Azeddien M. Sllame,: A Simulator For FAT-Tree Network-On-Chip Systems Based On MPLS Technique, In The Libyan International Conference on Electrical Engineering and Technologies (LICEET 2018), Tripoli, Libya, March 2018.
[7]	Azeddien M. Sllame, Asraa Hema, Huda Alnajem: Performance Comparison of VoIP over MPLS-Based Networks with RSVP Protocol Using SIP, H.323 Signaling Protocols, <i>In The Libyan International Conference on Electrical Engineering and Technologies</i> (LICEET 2018), Tripoli, Libya, March 2018.
[8]	Azeddien M. Sllame: Integrating LAB work with classes in computer network courses, Chapter 15, pp.253-275, Book Chapter in <i>The Future of Accessibility in International Higher Education</i> , Editors: Henry C. Alphin Jr, , Roy Y. Chan, and Jennie Lavine, IGI Global Publisher, DOI: 10.4018/978-1-5225-2560-8.ch015, USA, 2017.
[9]	Azeddien M. Sllame: Evaluating the Impact of Routing on QoS of VoIP over MANET Wireless Networks, In Open Access Library Journal (OALib Journal), Scientific Research Publishing, Volume 4:e3361, No. 2, Feb. 2017.
[10]	Azeddien M. Sllame, Nagwa Salama: An MPLS Based Fat Tree Network-On-Chip Systems, <i>In The IEEE 3rd International Conference on Advances in Computing & Communication and Engineering (ICACCE 2016)</i> , Durban, South Africa , pp. 46-53, (ISBN.987-1-5090-2576-6), 28-29 November 2016.
[11]	Azeddien M. Sllame, et al.: Performance Comparison of VoIP over IEEE 802.11(b,g) and WiMAX Wireless Network Technologies, <i>In International Journal of Wireless Communications and Networking Technologies (IJWCNT)</i> , Vol. 5 No. 5 (August - September), 2016.
[12]	Nagwa Salama, Azeddien M. Sllame: Designing an Efficient MPLS-Based Switch for FAT Tree Network-on-Chip Systems, <i>In ACM Proceedings of the 1st International Workshop on Advanced Interconnect Solutions and Technologies for Emerging Computing Systems (AISTECS workshop)</i> , Prague, Czech Republic, January 2016.

[13]	Azeddien M. Sllame, et al.: Performance Comparison of VoIP over Wireless Ad Hoc Networks Using Different Routing Protocols and Queuing Techniques, <i>In IEEE International Symposium on Networks, Computers and Communications (ISNCC 2015)</i> , Sousa Tunis, May 2015.
[14]	Azeddien M. Sllame, et al.: Performance Evaluation of Video Conferencing Over Ad Hoc Wireless Networks Using Different Routing and Queuing Techniques, <i>In The 15th annual Middle Eastern Simulation and Modelling Conference (MESM'15)</i> , Manama, Bahrain, 2-4 March 2015.
[15]	Azeddien M. Sllame, Mohamed Aljafry: Performance Evaluation of Multimedia over IP/MPLS Networks, <i>International Journal of Computer Theory and Engineering</i> , Vol. 7, No.4, pp.283-291, August 2015.
[16]	Azeddien M. Sllame, Mohamed Aljafry: Evaluating the Impact of Routing and Queuing Techniques on QoS of VoIP over IP/MPLS Networks, <i>International Journal of Innovation Research in Computer and Communication Engineering (www.IJIRCCE.com)</i> , Vol.2 issue 12, pp. 7130-7139, December 2014.
[17]	Azeddien M. Sllame: Modeling and Simulating MPLS Networks, <i>In IEEE International Symposium on Networks, Computers and Communications (ISNCC 2014)</i> , IEEE Catalog Number: CFP1468Y - USB, ISBN: 978-1-4799-5873-3, Tunis, June 2014.
[18]	Azeddien M. Sllame, Amani Hasan: A Comparative Study between Fat Tree and Mesh Network-on-Chip Interconnection Architectures, <i>In The 14th annual Middle Eastern Simulation and Modelling Conference (MESM'14)</i> , pp. 31-37, Muscat, Oman, 3-5 February 2014.
[19]	Azeddien M. Sllame, Mohamed Aljafry: Using Simulation and Modeling Tools in Teaching Computer Network Courses, <i>In IEEE International Conference on IT Convergence and Security, ICITCS'2013</i> , Macau, China, December 2013.
[20]	Azeddien M. Sllame, Asma Elasar: Modeling and Simulating Network-on-Chip Designs: A Case Study of Fat Tree Interconnection Architecture, <i>In International Journal of Computer Theory and Engineering</i> , pp. 823-829, Vol. 5, No. 5, October 2013.
[21]	Azeddien M. Sllame, Asma Elasar: An Efficient Switch for Fat Tree Network-on-Chip Interconnection Architecture, <i>In IEEE International Conference on Computer Systems and Industrial Informatics (ICCSII'12)</i> , Sharjah, United Arab Emirates, December 2012.
[22]	Asma Elasar, Azeddien M. Sllame: Evaluations of Fat Tree Based System on Chip Interconnection Architectures, <i>In Proc. of the 15th Int. IP/ESC 09 Conference, Grenoble, France, December 2009.</i>
[23]	Azeddien M. Sllame: EVO-EXPLORER: A Tool for Designing High-Performance Digital Systems, <i>In Proc. of the 4th Int. Conference on Innovations in Information Technology (IEEE explore library)</i> , Dubai, United Arab Emirates, November 2007.
[24]	Azeddien M. Sllame: A Model for a Reusable System-on-a-Chip Hardware Component Integrated with Design Exploration Methodology, <i>In Proc. of the 4th IEEE Int. Workshop on System-on-Chip for Real-Time Applications</i> , Banff, AB- Canada, July 2004.
[25]	Azeddien M. Sllame: Design Space Exploration Methodology for High-Performance System-on-a-Chip Hardware cores, <i>In Proc. of the 3rd IEEE Int. Workshop on System-on-Chip for Real-Time Applications</i> , Calgary, AB- Canada, June 2003.

[26]	Azeddien M. Sllame: <i>Design Space Exploration of High-Performance Digital Systems</i> , Faculty of Information Technology, Brno University of Technology, Ph.D. dissertation, 2003 , 125 pages, Brno, Czech Republic , ISBN. 80-214-2361-7.
[27]	Azeddien M. Sllame: A Pipeline Scheduling Algorithm for High-Level Synthesis, <i>In proc. of IFAC Workshop on Programmable Devices and Systems Conference PDS'03</i> , Ostrava, Czech Republic, February 2003 , pp. 178-183.
[28]	Azeddien M. Sllame, V. Drabek: Design Space Exploration Scheme for High-Level Synthesis Systems, <i>In Proc. of 36th Int. Conference Modelling and Simulation of Systems MOSIS'02</i> , Ostrava, Czech Republic, April 2002 , pp. 305-312.
[29]	Azeddien M. Sllame, L. Sekanina: An Evolutionary-Based Algorithm to the Module Selection Process in High-Level Synthesis, <i>In Proc. of 8th Int. Conference on Soft Computing Mendel'2002</i> , Brno, Czech Republic, June 2002 , pp. 87-92.
[30]	Azeddien M. Sllame: Efficient Design Space Characterization Toward Realizing High-Performance Digital Systems, <i>In Proc. of Electronic Devices and Systems Conference EDS'02</i> , Brno University of Technology, Brno, Czech Republic, September 2002 , p. 144-149.
[31]	Azeddien M. Sllame, V. Drabek: An Efficient List-Based Scheduling Algorithm for High-Level Synthesis, <i>In Proc. of IEEE EUROMICRO Symp. on Digital System Design: Architecture, Methods and Tools Conference DSD'2002</i> , Dortmund, Germany, September 2002 , pp.316-323.
[32]	Azeddien M. Sllame, L. Sekanina: An Evolutionary-Based Algorithm to the Module Selection Problem with Resource Sharing in High-Level Synthesis, <i>In Proc. of the Workshop on Real World Optimization Using Evolutionary Computing PPSN-2002</i> , Granada, Spain, September 2002 .
[33]	Azeddien M. Sllame, V. Drabek: Specification and Synthesis of Reusable Modules in VHDL, <i>In Proc. of IEEE 4th Design and Diagnostics of Electronic Circuits and Systems Workshop DDECS'01</i> , Gyor, Hungary, April 2001 , pp.137-140.
[34]	Azeddien M. Sllame: Modeling and Prototyping of Signal and Image Processing Algorithms in VHDL, <i>In Proc. of the 10th Int. Symp. on System-Modeling-Control SMC'2001</i> , Zakopane, Poland, May 2001 , pp. 201-205.
[35]	L. Sekanina, Azeddien M. Sllame: Toward Unified Approach to Design of Evolvable Hardware Based Systems, <i>In Proc. of 10th Int. Conference on Field Programmable Logic and Applications FPL2000</i> , Villach, Austria, September 2000 , pp.814-817.
[36]	Azeddien M. Sllame, V. Drabek: Design of Graphical Hardware, <i>In Proc. of 34th Int. Conference Modelling and Simulation of Systems MOSIS'2000</i> , Roznov pod Radhostem, Czech Republic, May 2000 , pp.51-55.
[37]	Azeddien M. Sllame: Design Exploration Based Reusability Method, <i>In Proc. of Student Conference STC UIVT'2000</i> , Brno University of Technology, Brno, Czech Republic, May 2000 , pp.240-242.
[38]	Azeddien M. Sllame, L. Sekanina: Simulation and Modeling of Evolvable Hardware Based Systems, <i>In Proc. of Int. Conference on Modeling and Simulation MS'2000</i> , Las Palmas de Gran Canaria, Spain, September 2000 , pp.485-492.

[39]	Azeddien M. Sllame: Designing on Algorithmic Level Using Behavioral Compiler, <i>In Proc. of 6th Electronic Devices and Systems Conference EDS'99</i> , Brno University of Technology, Brno, Czech Republic, 1999 , pp. 121-124.
[40]	Azeddien M. Sllame: Component Reuse and Recursivness in VHDL Modeling: A Practical Experience, <i>In Proc. of 33th Int. Conference Modelling and Simulation of Systems MOSIS'99</i> , Roznov pod Radhostem, Czech Republic, May 1999 , Vol. 2, pp. 51-58.
[41]	Azeddien M. Sllame: Usefulness of Component Reuse in Synthesizing VHDL Designs, <i>In IEEE Int. Workshop on Logic & Synthesis IWLS'99</i> , Granlibakken Resort, Lake Tahoe, California, USA, July 1999 .
[42]	Azeddien M. Sllame: Algorithmic Designs Using Behavioral Compiler, <i>In Student Conference FEI VUT</i> , Brno University of Technology, Brno, Czech Republic, July 1999 , pp. 107-108.
[43]	V. Dvorak, Azeddien M. Sllame: CSP-Based Modeling and Simulation in Computer Architecture Courses, <i>In CD-ROM of Proc. 9th Int. Symp. on System-Modeling-Control SMC'98</i> , Zakopane, Poland , May 1998, pp. 8/1-6.
[44]	Azeddien M. Sllame, V. Dvorak: An FPGA-Based Systolic Serial Multiplier, <i>In Proc. of the 5th Electronic Devices and Systems Conference EDS'98</i> , PC-DIR, Brno, Czech Republic, 1998 , pp.394-397.
[45]	Azeddien M. Sllame: <i>Simulation and Prototyping Systolic Arrays</i> , MSc Thesis, Brno University of Technology, Brno, Czech Republic , 1997, pp. 75.
[46]	Azeddien M. Sllame: Building of Information Systems Centers Based on Recent Advances on PC Technology, <i>In Proc. of the 1thLibyan Information Systems Symp.</i> , Tripoli, Libya, October 1995 , (in Arabic), pp.10.